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CONFIRMATION NO. 4507

SERIAL NUMBER 09/927,368	FILING OR 371(c) DATE 08/13/2001 RULE	CLASS 324	GROUP ART UNIT 2829	ATTORNEY DOCKET NO. 50090-332	
APPLICANTS Hisaya Mori, Hyogo, JAPAN; Shinji Yamada, Hyogo, JAPAN; Teruhiko Funakura, Tokyo, JAPAN;					
** CONTINUING DATA *****					
** FOREIGN APPLICATIONS ***** JAPAN 2001-032596 02/08/2001					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 09/17/2001					
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged _____ Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 11	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 2
ADDRESS 20277					
TITLE APPARATUS AND METHOD FOR TESTING SEMICONDUCTOR INTEGRATED CIRCUIT					
FILING FEE RECEIVED 1010	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		